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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HD74LS86

Quadruple 2-input Exclusive-OR Gates

REJ03D0422-0200 Rev.2.00 Feb.18.2005

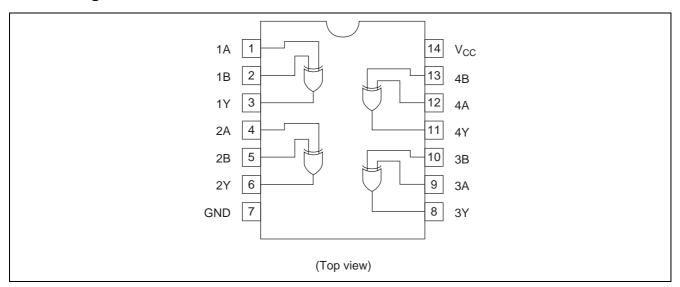
Features

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LS86P	DILP-14 pin	PRDP0014AB-B (DP-14AV)	Р	_
HD74LS86FPEL	SOP-14 pin (JEITA)	PRSP0014DF-B (FP-14DAV)	FP	EL (2,000 pcs/reel)
HD74LS86RPEL	SOP-14 pin (JEDEC)	PRSP0014DE-A (FP-14DNV)	RP	EL (2,500 pcs/reel)

Note: Please consult the sales office for the above package availability.

Pin Arrangement



Function Table

Inp	Outputs	
Α	В	Y
L	L	L
L	Н	Н
Н	L	Н
Н	Н	L

H; high level, L; low level

Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage	V _{CC}	7	V
Input voltage	V _{IN}	7	V
Power dissipation	P _T	400	mW
Storage temperature	Tstg	-65 to +150	°C

Note: Voltage value, unless otherwise noted, are with respect to network ground terminal.

Recommended Operating Conditions

Item	Symbol	Min	Тур	Max	Unit
Supply voltage	V_{CC}	4.75	5.00	5.25	V
Output current	I _{OH}	_		-400	μΑ
Output current	I _{OL}	_		8	mA
Operating temperature	Topr	-20	25	75	°C

Electrical Characteristics

 $(Ta = -20 \text{ to } +75 \text{ }^{\circ}\text{C})$

Item	Symbol	min.	typ.*	max.	Unit	Condition
Input voltage	V _{IH}	2.0	_	_	V	
Input voltage	V_{IL}	_	_	0.8	V	
Output voltage	V_{OH}	2.7			V	$V_{CC} = 4.75 \; V, \; V_{IH} = 2 \; V, \; V_{IL} = 0.8 \; V, \\ I_{OH} = -400 \; \mu A$
Output voltage	V _{OL}	_	_	0.4	V	$I_{OL} = 4 \text{ mA}$ $V_{CC} = 4.75 \text{ V}, V_{IH} = 2 \text{ V},$
		_	_	0.5		$I_{OL} = 8 \text{ mA}$ $V_{IL} = 0.8 \text{ V}$
Input current	I _{IH}	_	_	40	μΑ	$V_{CC} = 5.25 \text{ V}, V_{I} = 2.7 \text{ V}$
	I _{IL}	_	_	-0.8	mA	$V_{CC} = 5.25 \text{ V}, V_I = 0.4 \text{ V}$
	I _I	_	_	0.2	mA	$V_{CC} = 5.25 \text{ V}, V_I = 7 \text{ V}$
Short-circuit output current	Ios	-20	_	-100	mA	V _{CC} = 5.25 V
Supply current**	Icc	_	6.1	10	mA	V _{CC} = 5.25 V
Input clamp voltage	V _{IK}	_	_	-1.5	V	$V_{CC} = 4.75 \text{ V}, I_{IN} = -18 \text{ mA}$

Notes: $^*V_{CC} = 5 \text{ V}$, $Ta = 25^{\circ}C$

Switching Characteristics

 $(V_{CC} = 5 \text{ V}, \text{ Ta} = 25^{\circ}\text{C})$

Item	Symbol	Inputs	min.	typ.	max.	Unit	Test Conditions	
	t _{PLH}	A or B	_	12	23	ns		Other inputs = 0 V
Propagation	t _{PHL}	AUID	_	10	17	ns	$C_L = 15 \text{ pF},$ $R_L = 2 \text{ k}\Omega$	Other inputs = 0 v
delay time	t _{PLH}	A or B	_	20	30	ns		Other inputs = 4.5 V
	t _{PHL}		_	13	22	ns		

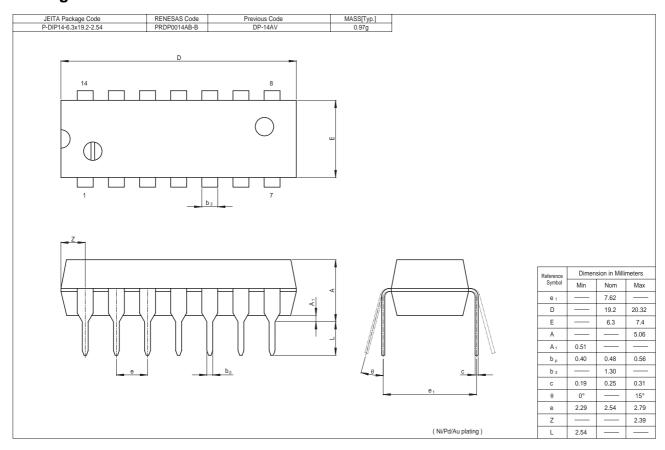
Note: Refer to Test Circuit and Waveform of the Common Item "TTL Common Matter (Document No.: REJ27D0005-0100)".

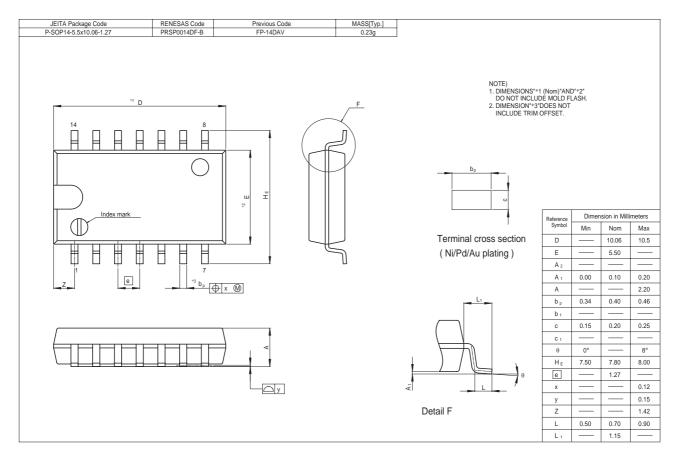


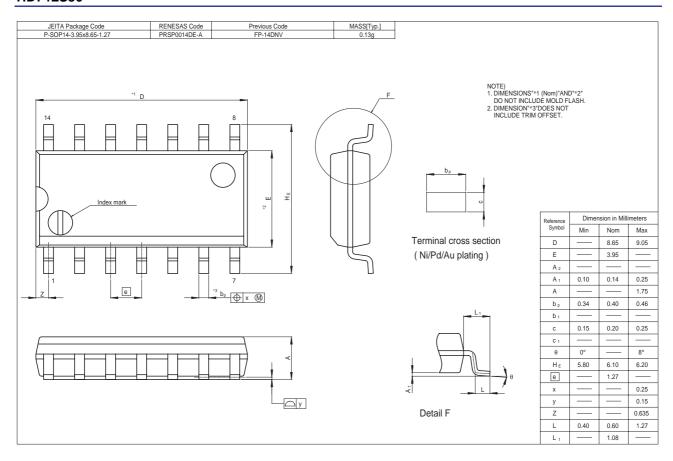


^{**} I_{CC} is measured with all outputs open and all other inputs grounded.

Package Dimensions







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